

# SPECIFICATION FOR APPROVAL


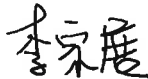
CUSTOMER :  
 CUSTOMER P/N :  
 ELIVEN P/N : TSPI-1770 series  
 DESCRIPTION : SMD INDUCTOR  
 APPLICATION : NON-AUTOMOTIVE  
 ISSUED DATE : 2020-05-02  
 RELEASE NOTE :

APPROVAL FEEDBACK		
REMARK:		

Maker

信華株式會社

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APPROVED	CHECKED	RECEIVED
今井禎宏		



# SINKA JAPAN CO.,LTD

PART NUM. : TSPI-1770 series

REVISION : A2.0

PAGE : 2 OF 13

REVISION	ECN NOTE	WAS	IS	DATE
A2.0	First Released			2020-05-02
Remarks				



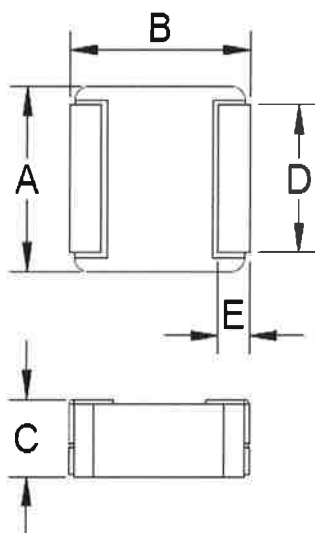
## A Features

- Low profile: Thickness 7.0 mm MAX
- Low loss with low DCR designs
- High performance (Isat) with low noise
- High operating frequency (up to 1MHz)
- RoHS and HF compliance

## B Applications

- DC to DC conversion circuit
- VRM for consumer or commercial application
- Laptop/personal computing application

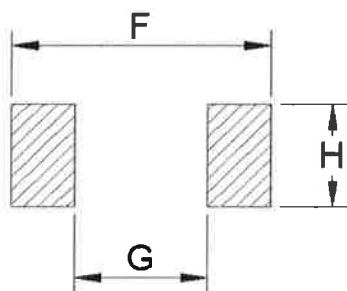
## C Dimensions



Dimensions	
A	16.8 ± 0.3
B	18.0 ± 0.3
C	6.7 ± 0.3
D	11.8 ± 0.3
E	2.7 ± 0.4

Unit : mm

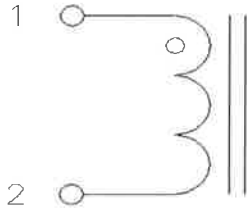
## D Recommended Land Pattern



Dimensions	
F	19.0
G	11.7
H	12.1

Unit : mm

**E Schematics**



**F Marking Demostration**

(F1) Marking

The inductance is coded and marked by 3 digits

Where -> 1.0μH -> 1R0

(F2) Orientation of Marking

"Horizontal alignment" to the parts, as shown in below:



**G Part Number and Demostrations :**

TSPI - 1770 - 1R0 M  
 (G1) (G2) (G3) (G4) (G5)

(G1) Series Code

Unique identification code(s) for each series of product

(G2) Size Code

1770 = 16 x 18 x 7 ([mm] ref.)

(G3) Inductance Code

R10 > 0.1μH // 1R0 > 1.0μH // 100 > 10μH // 101 > 100μH

(G4) Tolerance Code

J > ±5% // K > ±10% // M > ±20% // N > ±30%

(G5) Reserved Code for futher use

## H Electrical Characteristics

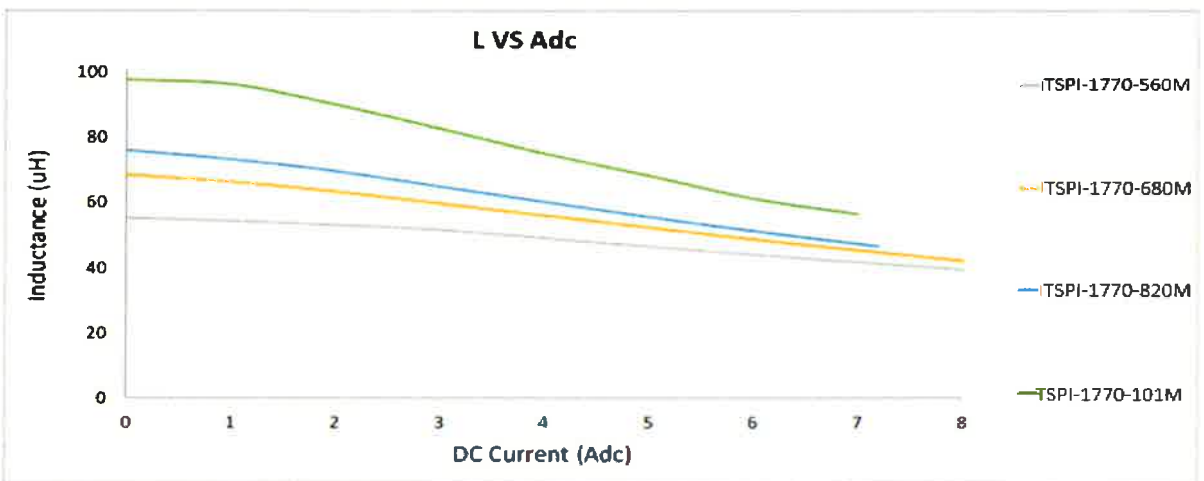
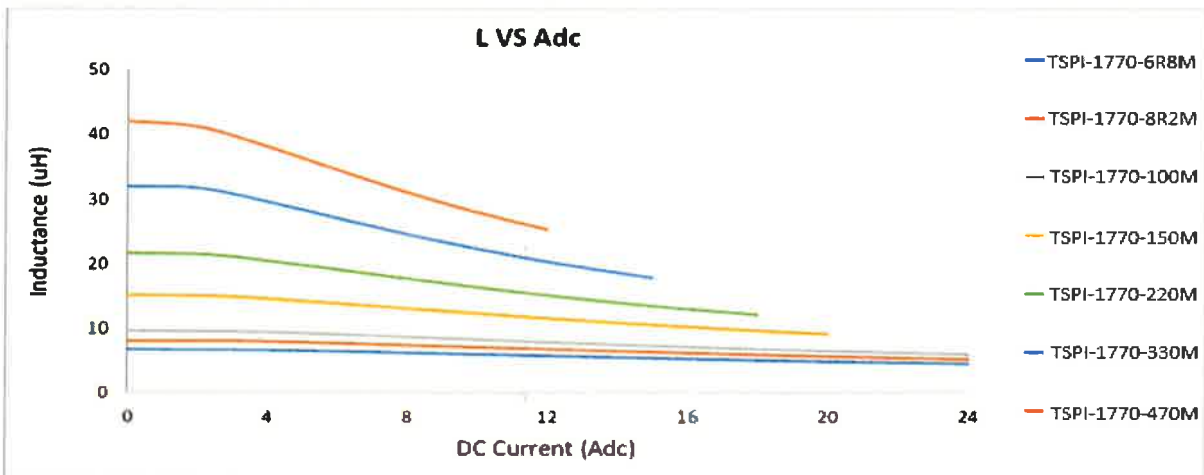
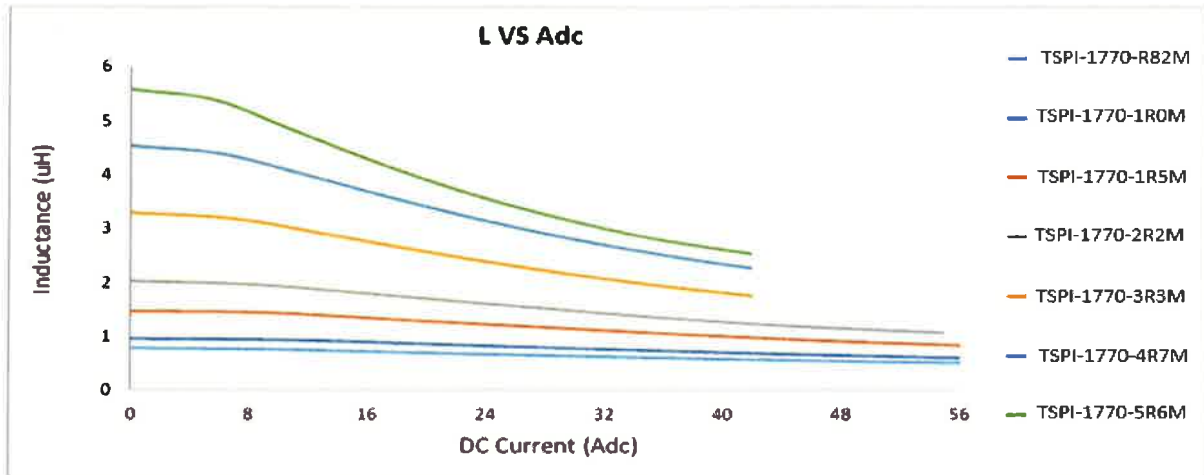
P/N	SelfLead (SL) LeadFrame (LF)	L0	DC		Heat Rating	Saturation	N/W (g) (REF)
		Inductance	Resistance		Current	Current	
		$\mu\text{H} \pm 20\%$	Rdc (m $\Omega$ )		Idc (AMP)	Isat (AMP)	
		@0A	Typical	Max	Typical	Typical	
TSPI-1770-R82M	LF	0.82	0.98	1.08	56.5	45	12
TSPI-1770-1R0M	LF	1.0	1.21	1.27	55.5	32	12
TSPI-1770-1R5M	LF	1.5	1.54	1.62	48	31	12
TSPI-1770-2R2M	LF	2.2	1.85	1.98	44	28	12
TSPI-1770-3R3M	LF	3.3	2.79	2.93	35	27	12
TSPI-1770-4R7M	LF	4.7	3.98	4.18	30	21	12
TSPI-1770-5R6M	LF	5.6	4.23	4.44	28	21	12
TSPI-1770-6R8M	LF	6.8	5.86	6.15	22.5	18.5	12
TSPI-1770-8R2M	LF	8.2	7.71	8.1	21	18	12
TSPI-1770-100M	LF	10	8.89	9.33	19	17	12
TSPI-1770-150M	LF	15	13.7	14.4	14	12	12
TSPI-1770-220M	LF	22	20.0	21	12	9.5	12
TSPI-1770-330M	LF	33	35.1	37	10.7	9	12
TSPI-1770-470M	LF	47	40.7	42.7	8.7	8.6	12
TSPI-1770-560M	LF	47	55.0	57.8	7.2	4.2	12
TSPI-1770-680M	LF	68	72.1	75.7	6.1	4.5	12
TSPI-1770-820M	LF	82	87.3	91.7	5.5	4.5	12
TSPI-1770-101M	LF	100	105	110	5	4	12

◆ Typical Heat Rating Current (Idc) - would cause an approximate  $\Delta T$  of 40°C

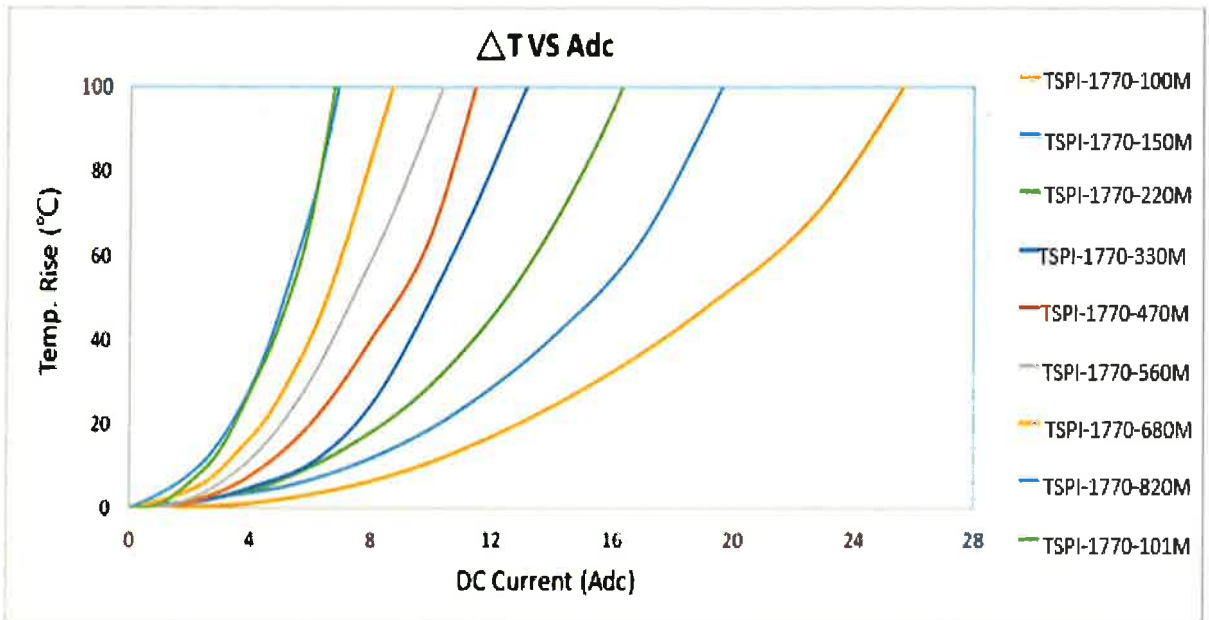
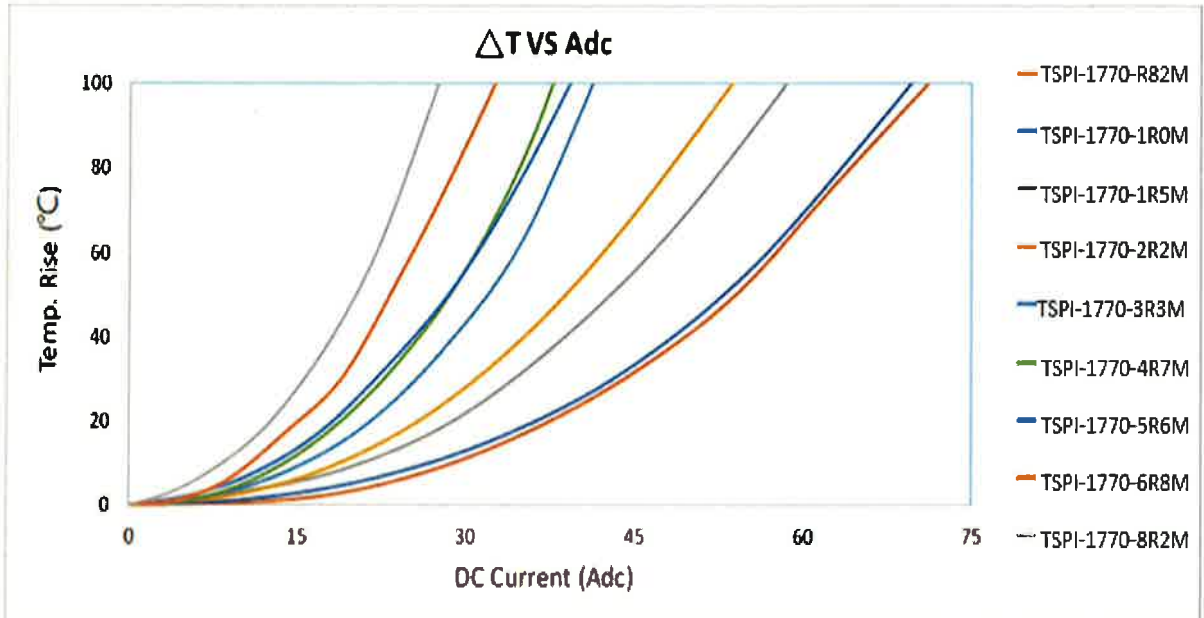
◆ Typical Saturation Current (Isat) - would cause (Lo) to drop approximately 30%



## | Performance Curve



**I Performance Curve**



**J General Information**

(J1) Testing Condition

25 °C , 60 % RH

(J2) Operating Temperature

-55 °C ~ 125 °C

(J3) Storage Condition

25 °C ~ 35 °C , < 70 % RH

(J4) Moisture Sensitive Level Class

MSL > 1

(J5) The Part temperature should not exceed 125°C under worst case operating condition.

Part temperature should be checked and verified by the application developer(s) as the performance of the part might be affected.



**K Reliability tests and referred standards****(K1) Electrical Performance Test**

- Spec ▶ Inductance : As listed in table "H" @ 100kHz/1.0V  
Tester ▶ WK3260B,HP4284A,CH3302  
Spec ▶ DCR : As listed in table "H" @ 25°C  
Tester ▶ CH16502 Micro-Ohm Meter  
Spec ▶ Isat : As listed in table "H"  
Tester ▶ WK3260B/WK3265B(BIAS)  
Spec ▶ Idc : As listed in table "H"  
Tester ▶ WK3260B/WK3265B(BIAS)

**(K2) Low Temperature Exposure (Storage)**

- Spec ▶ Inductance change within 20% without mechanical damage.  
Method ▶ JESD22-A119 - Condition "B"  
Setting : Store at -55°C -10/+0°C.  
Duration : 1000 ± 4 Hours.

**(K3) High Temperature Exposure (Storage)**

- Spec ▶ Inductance change within 20% without mechanical damage.  
Method ▶ MIL-STD-202G - Method "108A" / Condition "D"  
Setting : Store at 125°C ± 3°C.  
Duration : 1000 ± 4 Hours.

**(K4) Temperature Cycling**

- Spec ▶ Inductance change within 20% without mechanical damage.  
Method ▶ JESD22 - Method "JA-104" / Condition "H"  
▶ Cycling : -55°C(keep 30min) ▶ Transition Period(≤ 10 minute) ▶ 125°C  
125°C(keep 30min) ▶ Transition Period(≤ 10 minute) ▶ -55°C  
Duration : 1000 cycles.

**(K5) Biased Humidity**

- Spec ▶ Inductance change within 20% without mechanical damage.  
Method ▶ MIL-STD-202G - Method "103"  
Setting : 85°C / 85% RH Chamber with unpowered inductors.  
Duration : 1000 ± 4 Hours.

**(K6) Operational Life**

- Spec ▶ Inductance change within 20% without mechanical damage.  
Method ▶ MIL-STD-202G - Method "108A" / Condition "D"  
Setting : Apply Idc current with elevated ambient to make 125°C  
Duration : 1000 ± 4 Hours.

**(K7) Physical Dimension**

- Spec ▶ Refer to page 2, item "C Dimensions"  
Method ▶ JESD22 - Method "JB-100"  
Sample Size : 30 pcs

**(K8) Terminal Strength (SMD)**

- Spec ▶ Inductance change within 20% without mechanical damage.  
Method ▶ ACE-Q200 - Method "006"  
Setting : Mounted to PCB and apply 17.7N or 1.8kg force  
Duration : 60 + 1 Second.





## K Reliability tests and referred standards

### (K9) Mechanical Shock:

- Spec ▶ Inductance change within 20% without mechanical damage.
- Method ▶ MIL-STD-202G - Method "213" / Condition "C"
  - Peak force : 100G
  - Direction : Shocks in each direction along 3 perpendicular axes.

### (K10) Vibration:

- Spec ▶ Inductance change within 20% without mechanical damage.
- Method ▶ MIL-STD-202G - Method "201"
  - Frequency : 10Hz~55Hz~10Hz , 1 minute cycle.
  - Duration : Approximately 2 hours.

### (K11) Resistance to Soldering Heat:

- Spec ▶ Inductance change within 20% without mechanical damage.
- Method ▶ MIL-STD-202G - Method "210" / Condition "J"
  - Setting : Reflow Peak  $235 \pm 5^{\circ}\text{C}$ ( $30 \pm 5\text{s}$ ) / Time above  $183^{\circ}\text{C}$ ( $90 \sim 120\text{s}$ )
  - Heat Cycles : 3

### (K12) Solderability

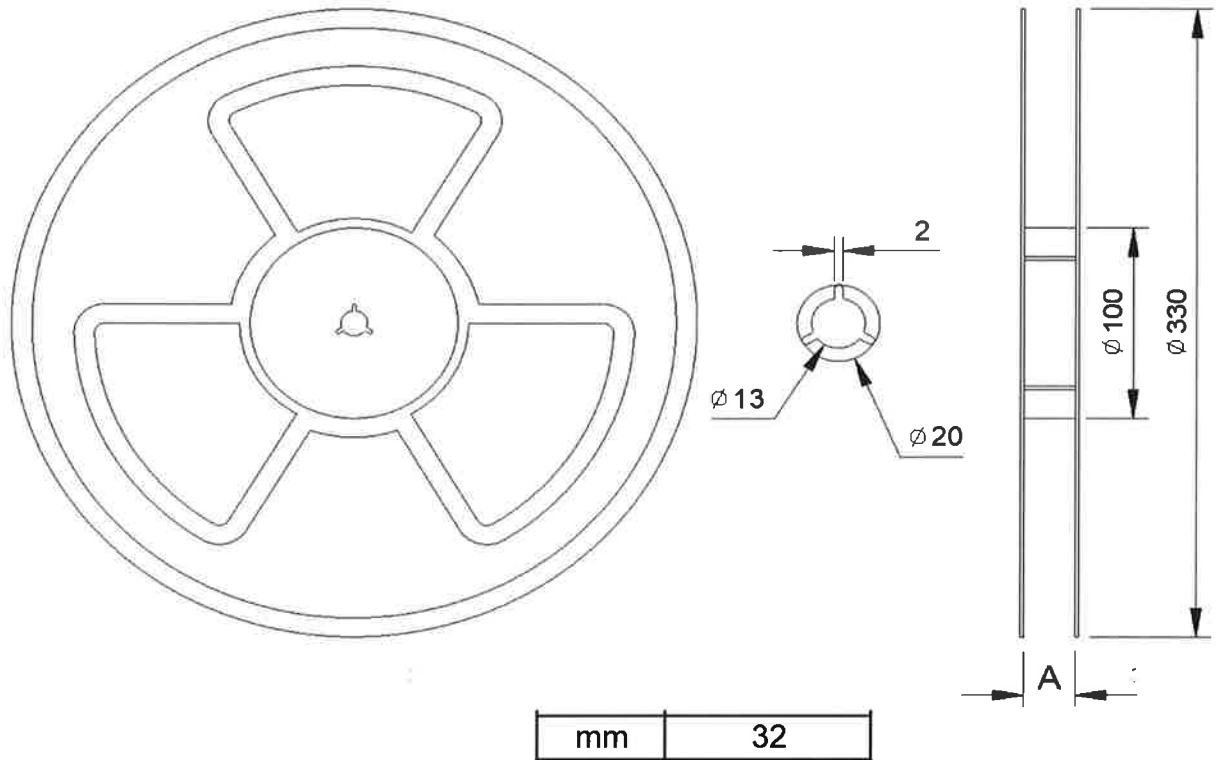
- Spec ▶ The surface of terminals tested shall be covered with new solder by 95%.
- Method ▶ J-STD-002D - Precondition "Category B" / Testing parameter "Table B1"
  - Preheating :  $150 \pm 10^{\circ}\text{C}$  60 seconds
  - Soldering :  $245 \pm 5^{\circ}\text{C}$  for 5 +0/-0.5 seconds

### (K13) Board Flex:

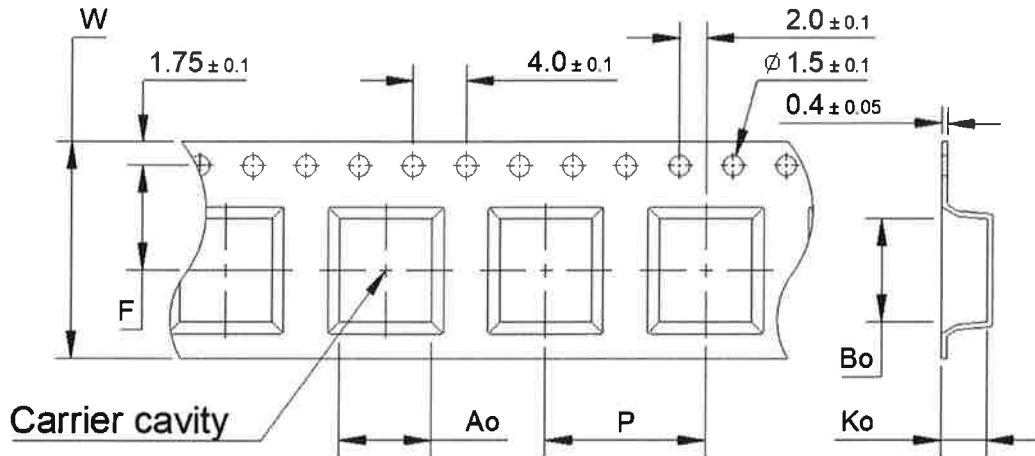
- Spec ▶ Inductance change within 20% without mechanical damage.
- Method ▶ ACE-Q200 - Method "005"
  - Setting : 2mm minimum downward displacement of PCB board.
  - Duration : 60 + 5 Seconds.

## L Packing Information :

### (L1) Reel Dimensions



### (L2) Carrier Tape Dimensions

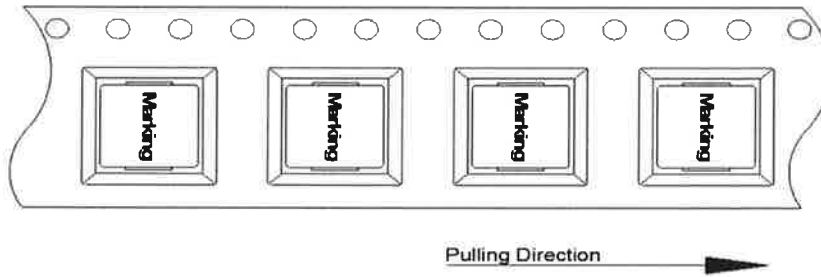


	$Ao$	$Bo$	$Ko$	$F$	$P$	$W$
mm	$17.45 \pm 0.1$	$19.1 \pm 0.1$	$7.9 \pm 0.1$	$14.2 \pm 0.1$	$24.0 \pm 0.1$	$32.0 \pm 0.3$

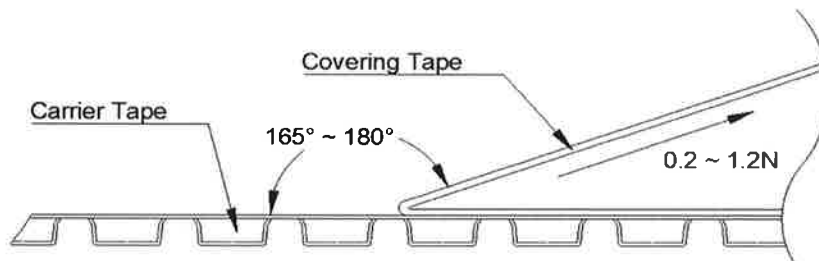
	Lead-OUT (Leader)	Packing Quantity	Lead-IN (Trailer)
cavity	> 400mm	150 PCS	> 180mm

## L Packing Information :

(L3) Taping Direction



(L4) Peel Force of Top Covering Tape

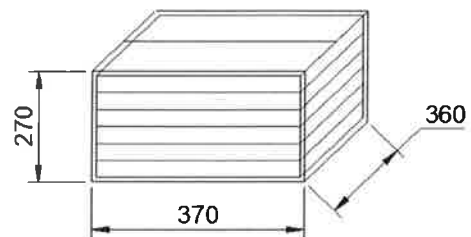
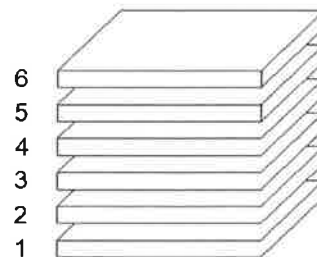


(L5) Packing Quantity

3 / Three Reels / Carton

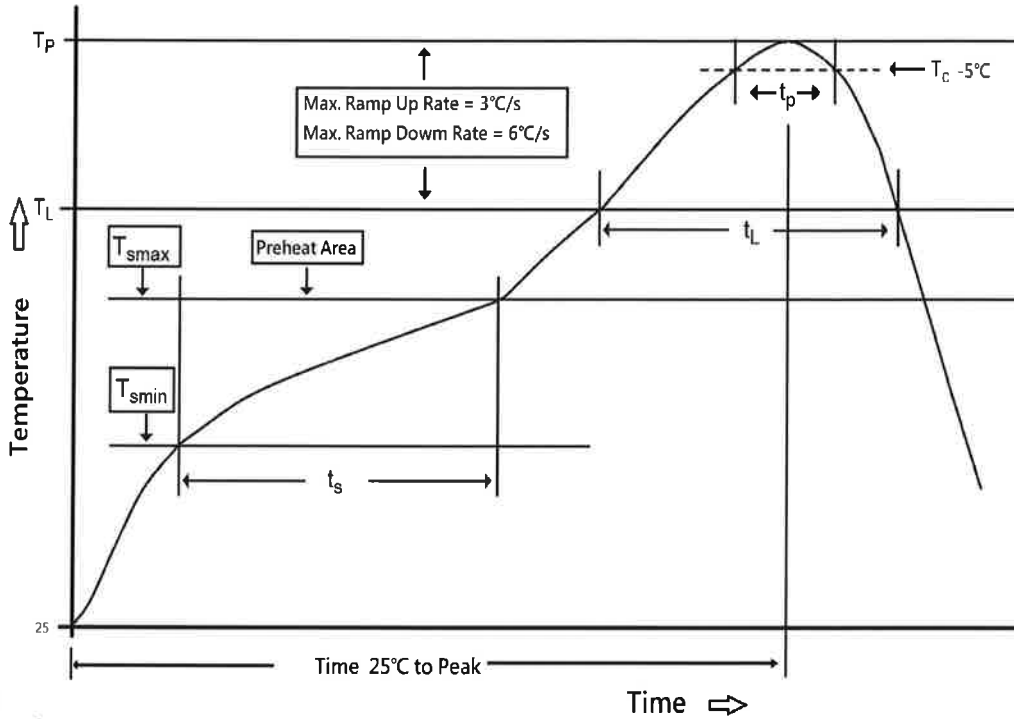
150 / 0.15k Pieces / Reel

450 / 0.45k Pieces / Carton



**M Soldering Specifications :**

(M1) Classification Reflow Profile for SMT Components



(f)

Profile Feature	Pb-Free Assembly
Preheat	
- Temperature Min( $T_{smin}$ )	150°C
- Temperature Max( $T_{smax}$ )	200°C
- Time ( $t_s$ ) from ( $T_{smin}$ to $T_{smax}$ )	60-120 seconds
Ramp-up rate ( $T_L$ to $T_P$ )	3°C/ second max.
Liquidous temperature( $T_L$ )	217°C
Time ( $t_L$ ) maintained above $T_L$	60-150 seconds
Peak package body temperature( $T_P$ )	See Table M3
Time within 5°C of actual peak temperature ( $t_p$ )	20-30 seconds
Ramp-down rate ( $T_P$ to $T_L$ )	6°C/ second max.
Time 25°C to peak temperature	8 minutes max.
Number of Reflow cycles allowed	2 cycles max.

refer to J-STD-020D

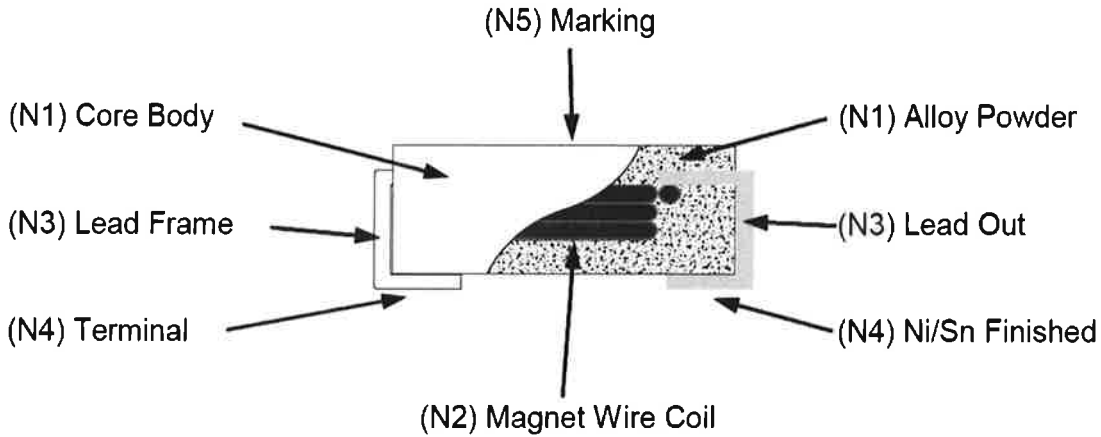
(M3) Package Classification Reflow Temperature

	Package Thickness	Volume mm <sup>3</sup>	Volume mm <sup>3</sup>	Volume mm <sup>3</sup>
		<350	350 - 2000	>2000
PB-Free Assembly	< 1.6mm	260°C	260°C	260°C
PB-Free Assembly	1.6-2.5mm	260°C	250°C	245°C
PB-Free Assembly	≥ 2.5mm	250°C	245°C	245°C

refer to J-STD-020D



**N Material Breakdown and Listing :**



No.	ITEM	MATERIAL DESCRIPTION	SOURCE	UL
N1	Core Body	Alloy Powder / FeSiCr	AVEN	N/A
N2	Magnet Wire	Polyamide-imide enameled Cu Wire / AIW	PEWC	E201757
N3	Lead Frame	Copper Lead Frame	CHIEF	N/A
N4	Terminal	Dual-layer Plating / Sn over Ni	CHIEF	N/A
N5	Marking	Ketone based black ink	M. IMAJE	N/A

**O Care Note :**

**(O1) Carton Handling :**

Shipping by carton(s) without a pallet is allowed if handled with care.

Points below MUST be enforced for carton handling:

1. Cartons are NOT to be placed in up-side-down position at all time.
2. Cartons are NOT to be placed in vertical position at all time.
3. Cartons are to be handled by hand only, any damage on carton may affect the integrity of components held inside.
4. Inspection of components is required upon carton deformation.
5. Carton(s) MUST NOT be exposed under direct sun light for storage.

Stacking of cartons is allowed but deformation of carton shall be avoid.

**(O2) Washing Detergent :**

All cleaning agent including pure water may compromise the integrity of the component(s), Please get confirmation before any use of washing detergent.

**(O3) Extra Infomation :**

Please contact authorized distributor for additional information that is not listed in this document.